

S.No.	ROLL NO.	NAME OF STUDENT	SUBJECT NAME												Grade	SPI	
			DCE-102			DC-912			DC-914			DCM-911					
			TE	TS	Total	TE	TS	Total	TE	TS	Total	TE	TS	Total			
		M.M	70	30	100	70	30	100	70	30	100	70	30	100			
1	6604331001	Richa Tiwari	34	25	59	36	24	60	33	26	59	45	24	69	B	6.5	
2	6604331002	Sudhanshu Upadhyay	28	21	49	32	25	57	23	26	49	25	22	47	D	4.5	
3	6604331003	Garima Gupta	35	23	58	40	28	68	39	27	66	25	23	48	D	6.5	
4	6604331004	Maneesh Upadhya	6	18	24	10	20	30	7	23	30	15	19	34	F	0.0	
5	6604331005	Manish Kumar Vishnoi	39	24	63	35	26	61	21	25	46	49	19	68	B	6.0	

Theory Subject Name :

DCE 102 - Advanced Digital Signal Processing

DC 912 - Digital Communication

DC 914 - Information Theory & Coding

DCM-911 Representation & Analysis of Random Signals

Checked By: 


Controller of Examination
U.P. Technical University Lucknow

U.P. Technical University, Lucknow
 JSS ACADEMY OF TECHNICAL EDUCATION NOIDA
 TABULATION CHART 2006-07
 Course : M.Tech (Modular) Branch : VLSI Design
 I SEMESTER

Centre Code : 091

S.No.	ROLL NO.	NAME OF STUDENT	SUBJECT NAME												VLS-914		SPI
			VLS-911			VLS-912			VLS-913			VLS-914			Total	Grade	
			TE	TS	Total	TE	TS	Total	TE	TS	Total	TE	TS	Total			
			M.M	70	30	100	70	30	100	70	30	100	70	30	100		
1	6609131006	Alka Goyal		52	29	81 A	32	24	56 C	32	29	61 C	45	27	72 B		7.5
2	6609131007	Saurabh Kumar Singh		28	18	46 D	10	20	30 F	28	15	43 E	28	21	49 D		2.5
3	6609131008	Anu Sharma		53	27	80 A	32	23	55 C	21	27	48 D	45	26	71 B		7.0
4	6609131009	Dushyant Bansal		45	18	63 C	23	20	43 E	23	18	41 E	40	22	62 C		4.0
5	6609131010	Prachi Mohan Kulshrestha		41	25	66 B	35	24	59 C	28	23	51 D	28	26	54 D		5.5
6	6609131011	Indra Jeet		25	23	48 D	23	16	39 E	20	18	38 E	41	21	62 C		3.5
7	6609131012	Kalpna Sharma		53	28	81 A	31	23	54 D	35	25	60 C	58	26	84 A		7.5
8	6609131013	Gunjan Gupta		42	23	65 B	23	22	45 D	28	20	48 D	44	20	64 C		5.5
9	6609131014	Himani Mittal		50	21	71 B	23	22	45 D	30	19	49 D	40	25	65 B		6.0
10	6609131015	Sachin Dubey		34	23	57 C	21	19	40 E	38	22	60 C	39	22	61 C		5.0
11	6609131016	Praveer Saxena		53	29	82 A	36	26	62 C	34	27	61 C	39	28	67 B		7.5
12	6609131017	Vipul Chauhan		21	22	43 E	11	20	31 F	24	18	42 E	9	21	30 F		1.0
13	6609131018	Sujata Rai		24	21	45 D	14	20	34 F	18	15	33 F	30	22	52 D		2.0
14	6609131019	Sukhdeep Kaur		39	26	65 B	29	19	48 D	36	22	58 C	30	23	53 D		5.5
15	6609131020	Dheerendra Singh Gangwar		33	25	58 C	16	26	42 E	35	26	61 C	18	21	39 E		4.0
16	6609131021	Arvind Kumar		35	26	61 C	21	19	40 E	22	22	44 E	32	24	56 C		4.0
17	6609131022	Naveen Kr. Saini		57	26	83 A	27	21	48 D	29	22	51 D	28	24	52 D		5.5

Theory Subject Name :

VLS-911 VLSI Design
 VLS-912 VLSI Technology
 VLS-913 HDL Based FPGA Design
 VLS-914 Modeling of Microelectronic Devices

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